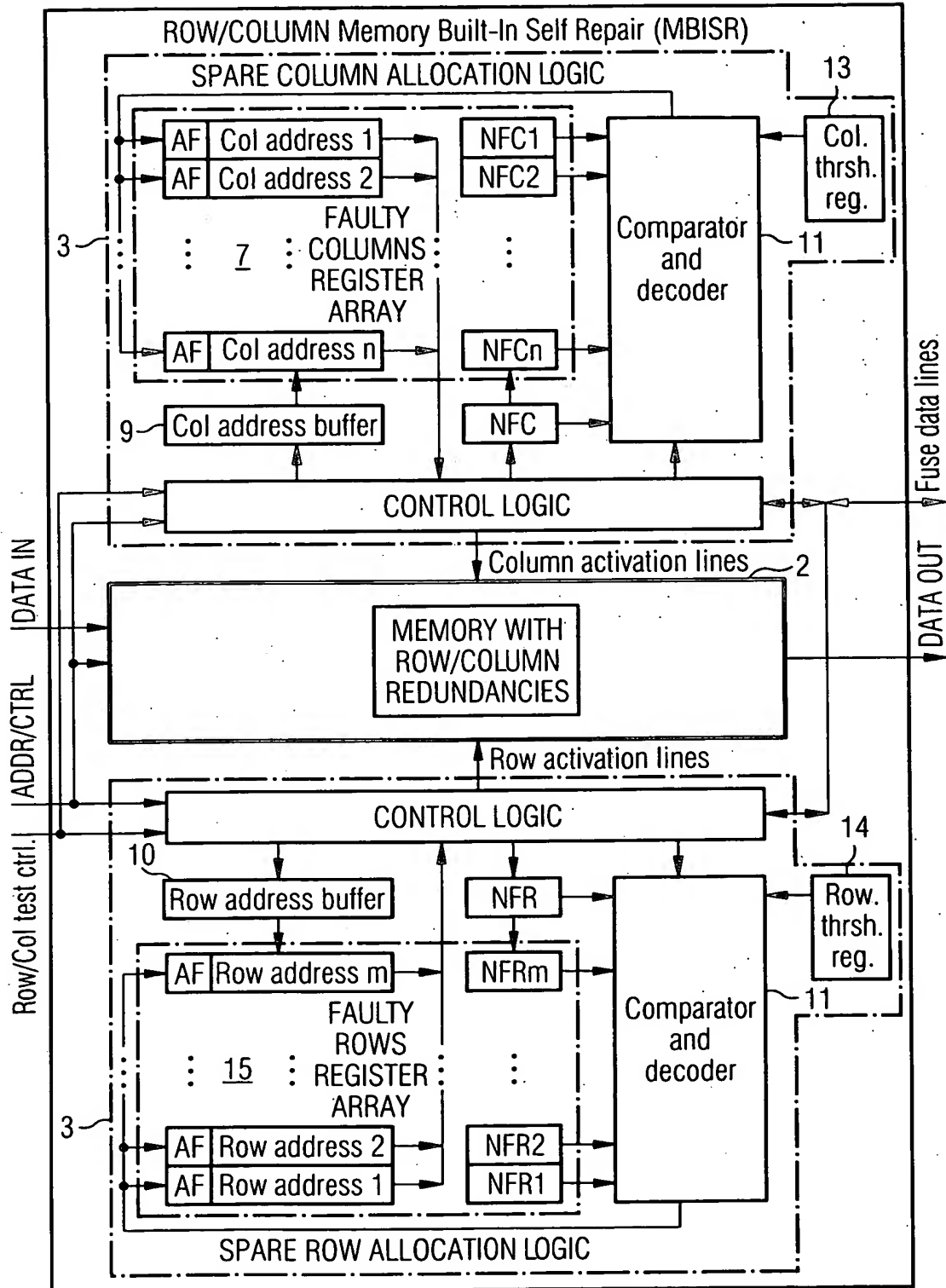


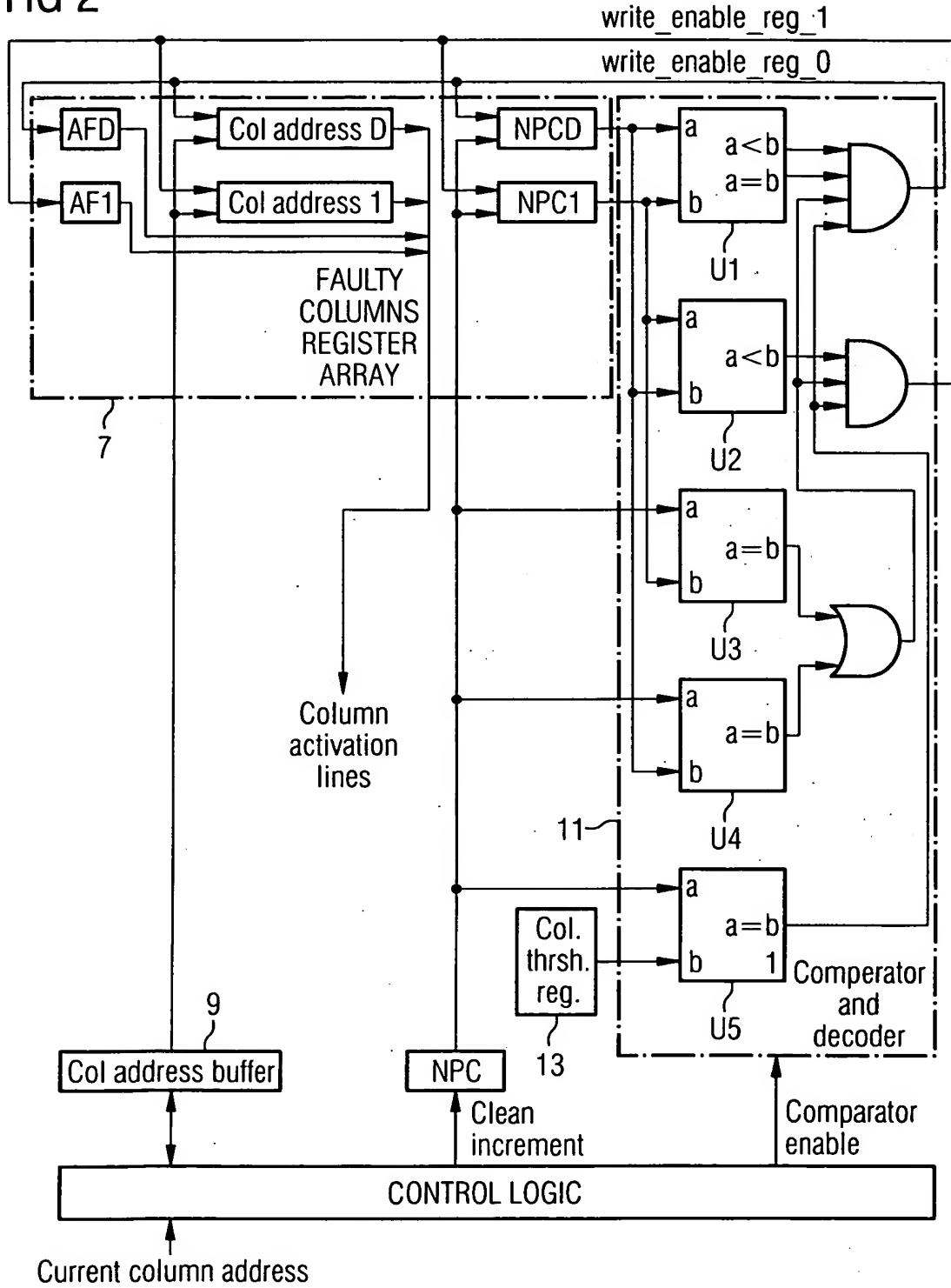
1/8

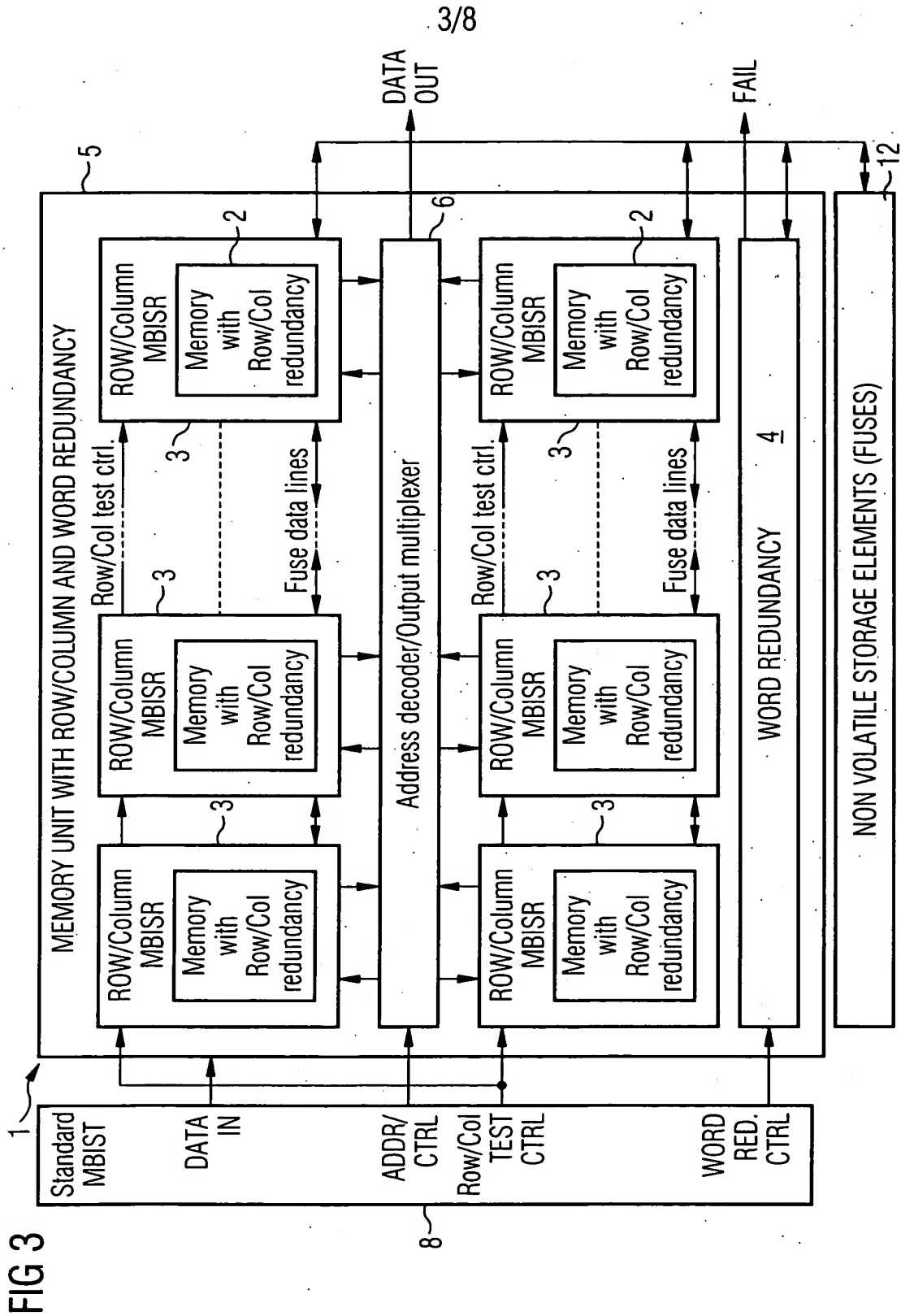
FIG 1



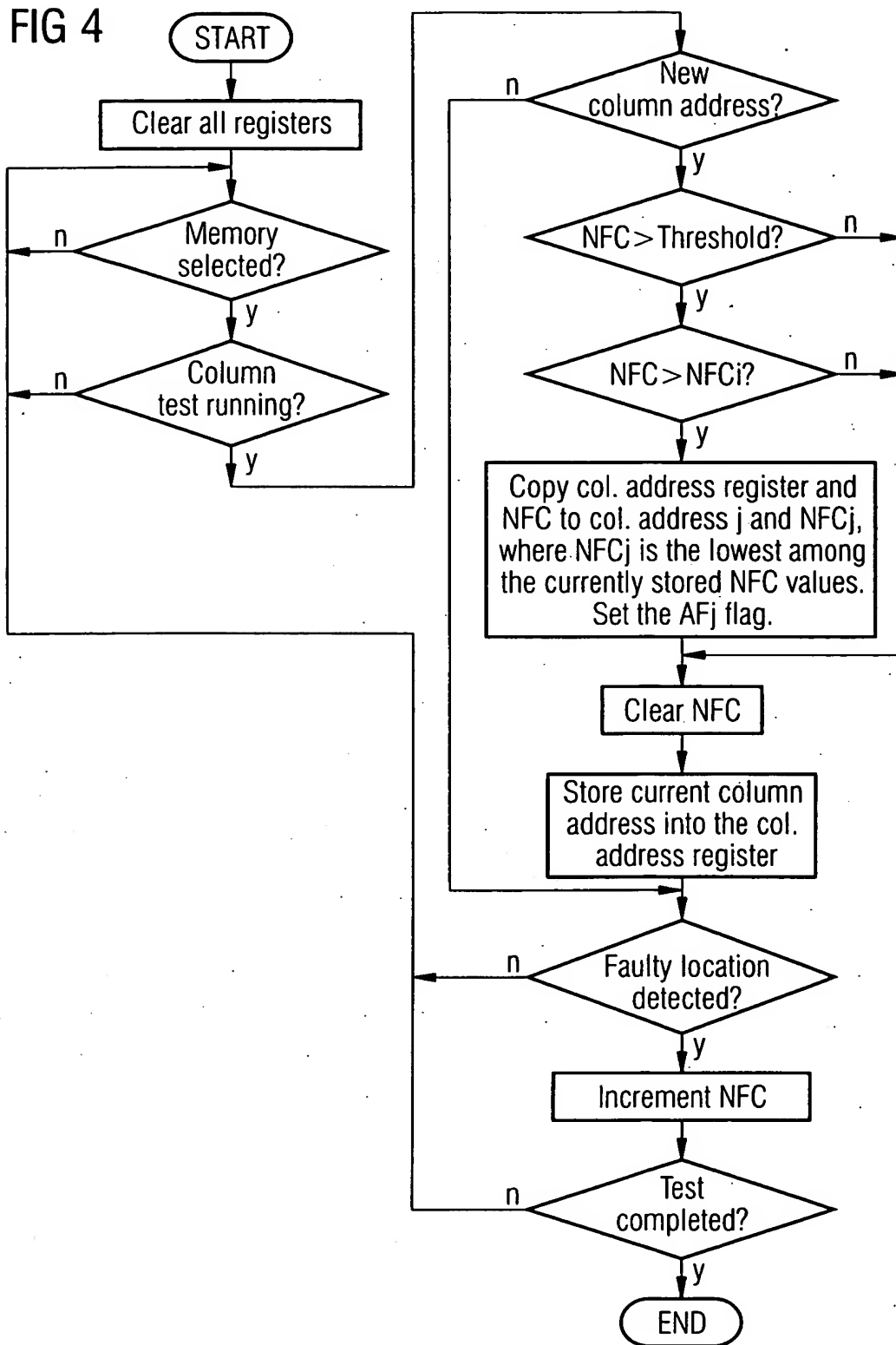
2/8

FIG 2

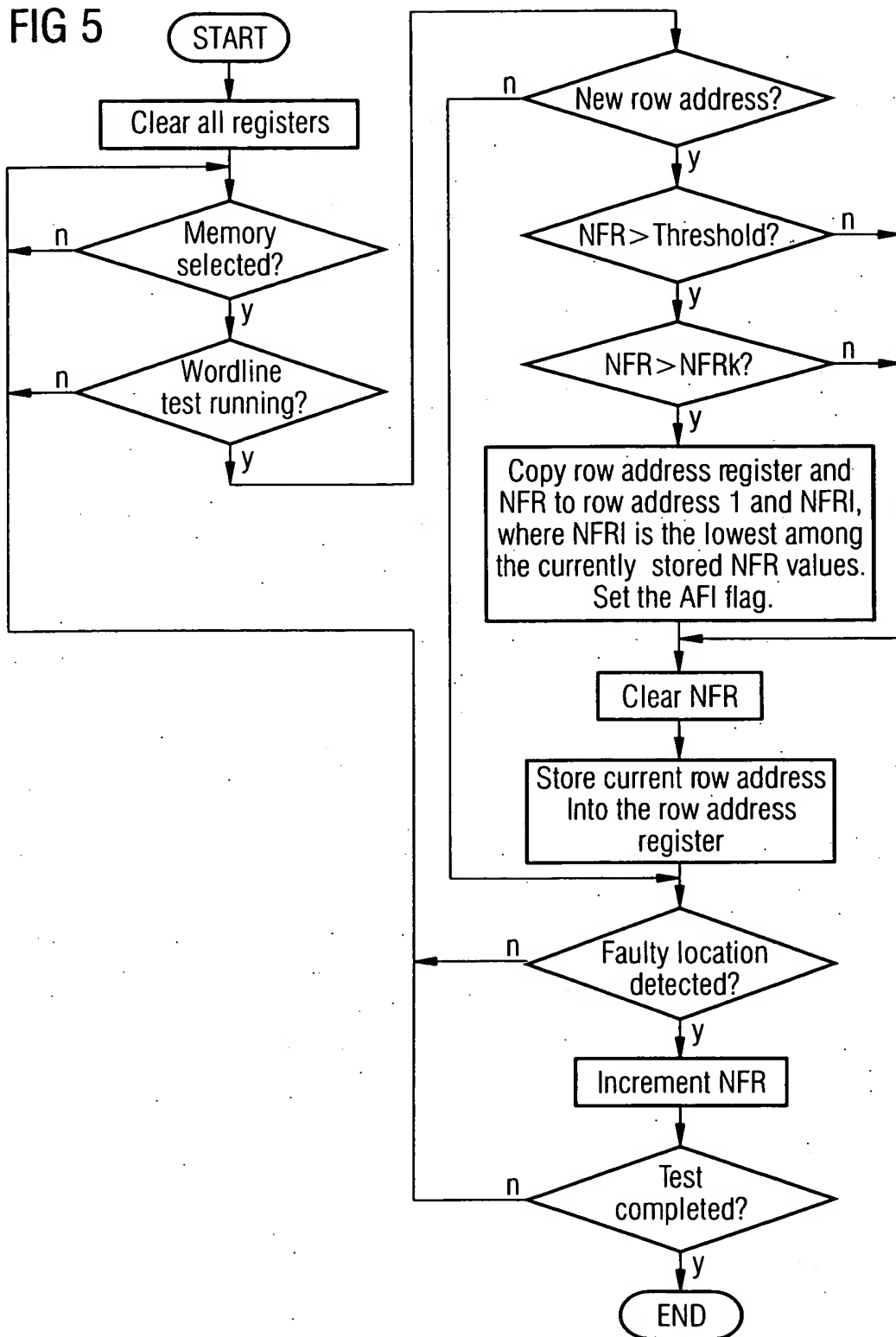




4/8

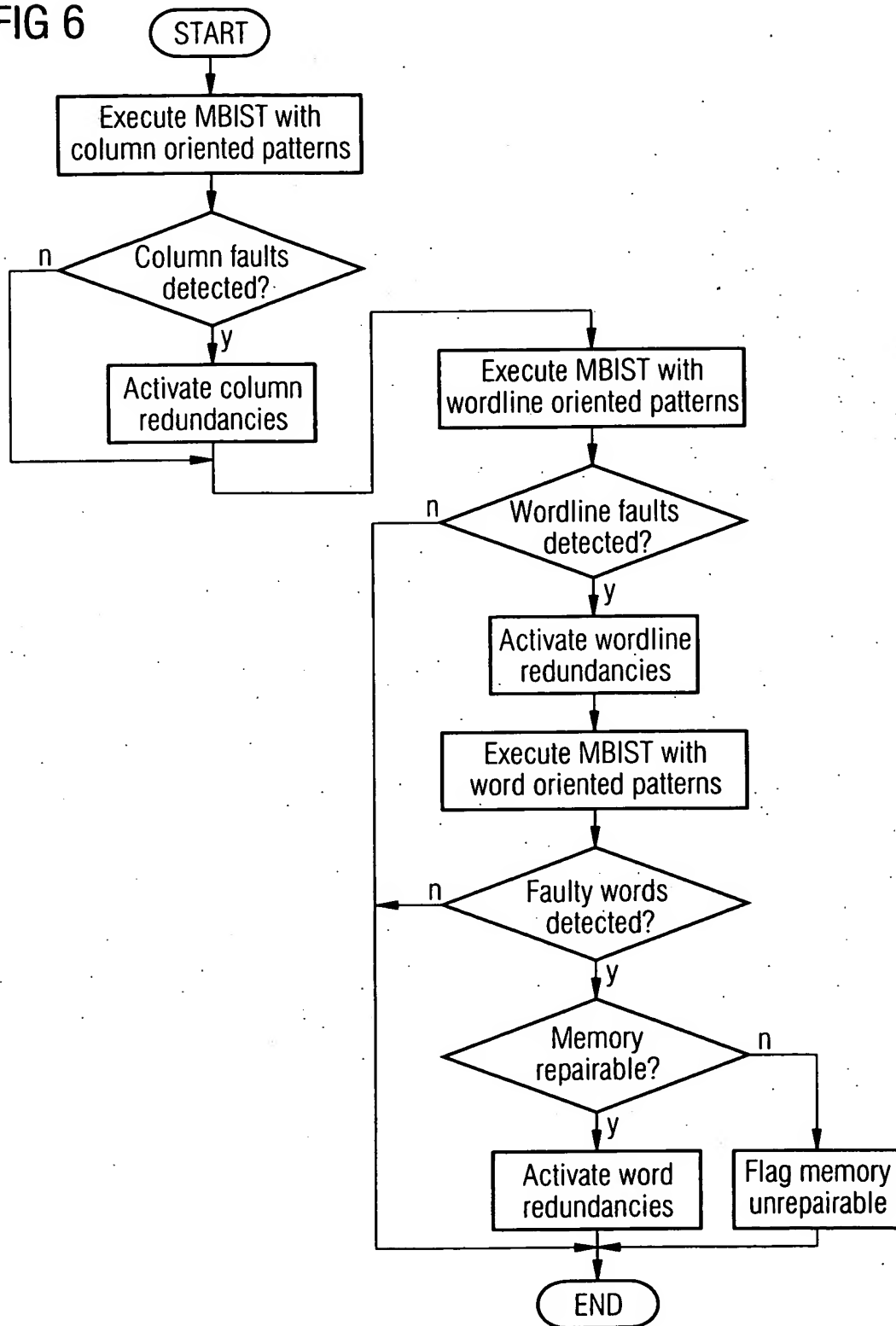


5/8



6/8

FIG 6



7/8

FIG 7

MEMORY ARRAY

	W0				W1				W2				W3				W4				W5				W6			
	C0	C1	C2	C3	C4	C5	C6	C7	C8	C9	C10	C11	C12	C13	C14	C15	C16	C17	C18	C19	C20	C21	C22	C23	C24	C25	C26	C27
R0																												
R1																												
R2																												
R3																												
R4																												
R5																												
R6																												
R7																												
R8																												
R9																												
R10																												
R11																												
R12																												
R13																												
R14																												
R15																												
R16																												
R17																												
R18																												
R19																												
R20																												
R21																												
R22																												
R23																												
R24																												
R25																												
R26																												
R27																												
R28																												
R29																												
R30																												
R31																												
R32																												
R33																												
R35																												
R35																												

□ = Fault-free cell  
 ▨ = Faulty cell

8/8

FIG 8

